

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S140	8076	Powerpc or power pc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 17:55
S141	2	implementation dependent layer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 17:55
S142	0	incompatible with (apic or advanced programmable interrupt controller\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 17:57
S144	1797	(apic or advanced programmable interrupt controller\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 18:38
S145	1	S144 same incompatible	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 17:59
S146	5	interrupt controller\$2 same incompatible	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 17:59
S149	34	S144 with (compatible or compatibility)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 18:38

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S150	69	S144 same (compatible or compatibility)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/02 18:38
S151	13	(APIC (id or identification) register\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/03 10:33
S152	0	midi same soundcard and (motorola and intel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 14:01
S153	244	midi and (motorola and intel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 14:08
S154	1184	compatible\$2 with (processor\$2 or cpu\$2) same convert\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 14:09
S155	0	S154 and (advanced programmable interrupt controller or APIC)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 14:17
S156	125	interrupt architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 14:17
S157	14	interrupt (method\$2 or architecture\$2) same (APIC or advanced programmable interrupt controller\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 15:56

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S158	15	(openpic or open pic or (open programmable interrupt controller)) and (apic or (advanced programmable interrupt controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 15:58
S159	55	(openpic or open pic or (open programmable interrupt controller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 16:11
S160	2239	abstraction layer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 16:12
S161	253	710/266.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/03 16:12
S162	79	710/260-266.ccls. and (powerpc or power pc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/04 09:00
S163	14171	(pass\$3 or forward\$3) near3 interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/04 09:01
S164	76	S163 same (compatible or compatibility)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	OFF	2006/05/04 09:04
S165	2753	interrupt\$2 with translat\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:27

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S166	26	S165 same (APIC or intel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:12
S167	299	interrupt\$2 same architectur\$3 same (cpu\$2 or processor\$2) same intel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:27
S168	69	S167 and (powerpc or motorola)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:27
S169	24	S167 same (powerpc or motorola)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:39
S170	260	710/262.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:39
S171	253	710/266.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:40
S172	462	S170 or S171	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:40
S173	83	S172 and interrupt\$2 same (compatible or compatibilit\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 14:54

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S174	51	S172 and interrupt\$2 same (compatible or compatibilit\$3) same (cpu\$2 or processor\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 15:04
S175	26	(apic) with design\$3 same interrupt\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/08 15:05
S176	2	("6006301").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/05/09 19:32
S177	9060	(interrupt\$2 near1 rout\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/09 19:33
S178	81	S177 and apic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/09 19:33
S179	15	S178 and (RISC or powerpc or power pc or power-pc)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	ADJ	ON	2006/05/09 19:34